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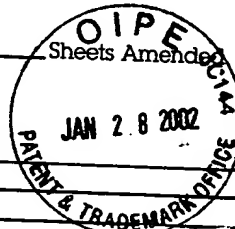
Applicant or Patentee Paul Fugger et al.

No. (Application, Appeal, Interference, Patent, Reexam) 09/935,791

Filing or Issue Date Aug 22, 2001

Title: Addition circuit for digital data

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